intersil

DATASHEET

Radiation Hardened Programmable Low Power Op Amps

HS-3530ARH, HS-3530AEH

The HS-3530ARH, HS-3530AEH are low power operational amplifiers, which are internally compensated monolithic devices offering a wide range of performance specifications. Parameters such as power dissipation, slew rate, bandwidth, noise and input DC parameters are programmed by selecting an external resistor or current source. Supply voltages as low as ±3V may be used with little degradation of AC performance. Operation from -55°C to +125°C is guaranteed.

A major advantage of the HS-3530ARH, HS-3530AEH is that operating characteristics remain virtually constant over a wide supply range (±3V to ±15V), allowing the amplifiers to offer maximum performance in almost any system, including battery operated equipment. A primary application for these devices is in active filtering and conditioning for a wide variety of signals that differ in frequency and amplitude. Also, by modulating the set current, they can be used for designs such as current controlled oscillators/modulators, sample and hold circuits and variable active filters.

Specifications

Specifications for Rad Hard QML devices are controlled by the Defense Logistics Agency Land and Maritime (DLA). The SMD numbers listed below must be used when ordering.

Detailed Electrical Specifications for the HS-3530ARH. HS-3530AEH are contained in SMD 5962-95687. A "hot-link" to the DLA website is also provided on the Product Information page for downloading the document. www.intersil.com/products/hs-3530arh

The Intersil Quality Management Plan, listing all screening operations, is available on our website. www.intersil.com/design/quality/manuals.asp

Ordering Information

ORDERING NUMBER	PART NUMBER	TEMP. RANGE (°C)	
5962F9568701QGA	HS2-3530ARH-8	-55 to +125	
5962F9568701VGA	HS2-3530ARH-Q	-55 to +125	
5962F9568702VGA	HS2-3530AEH-Q	-55 to +125	
5962F9568701VXC	HS9-3530ARH-Q	-55 to +125	
5962F9568702VXC	HS9-3530AEH-Q	-55 to +125	
5962F9568701V9A	HS0-3530ARH-Q	-55 to +125	
5962F9568702V9A	HS0-3530AEH-Q	-55 to +125	
HS2-3530ARH/PROTO	HS2-3530ARH/Proto	-55 to +125	
HS9-3530ARH/PROTO	HS9-3530ARH/Proto	-55 to +125	
HS0-3530ARH/SAMPLE	HS0-3530ARH/SAMPLE	-55 to +125	

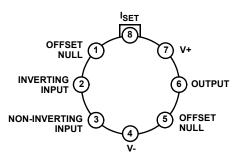
Features

 Radiation performance Single event latch-up Immune (RSG DI Process) High dose rate (50-300rad(Si)/s) 300krad(Si) Low dose rate (0.01rad(SI)/s) 50krad(Si)
 Wide range AC programming Slew rate
 Wide range DC programming Power supply range ±3.0V to ±15V
• Supply current
Output current
• Quiescent power
Dielectrically isolated device islands
Observations the second second

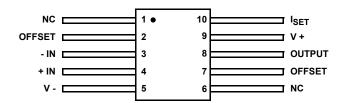
- · Short circuit protection
- Full -55°C to +125°C military temperature range

Pin Configurations

HS2-3530ARH, HS2-3530AEH (CAN), MACY1-X8 TOP VIEW



HS9-3530ARH, HS9-3530AEH(FLATPACK), CDFP3-F10 TOP VIEW



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CAUTION: These devices are sensitive to electrostatic discharge; follow proper IC Handling Procedures. 1-888-INTERSIL or 1-888-468-3774 | Copyright Intersil Americas LLC 1999, 2012, 2013, 2015. All Rights Reserved Intersil (and design) is a trademark owned by Intersil Corporation or one of its subsidiaries. All other trademarks mentioned are the property of their respective owners.

Die Characteristics

DIE DIMENSIONS:

 $1720\mu m \ x \ 1390\mu m \ x \ 533\mu m \ \pm 25.4\mu m$ (68 mils x 55 mils x 21 mils ± 1 mil)

INTERFACE MATERIALS

GLASSIVATION

Type: Silox (SiO₂) Thickness: 8.0kA ±1.0kA

TOP METALLIZATION

Type: AlSiCu Thickness: 16.0kA ±2kA

SUBSTRATE:

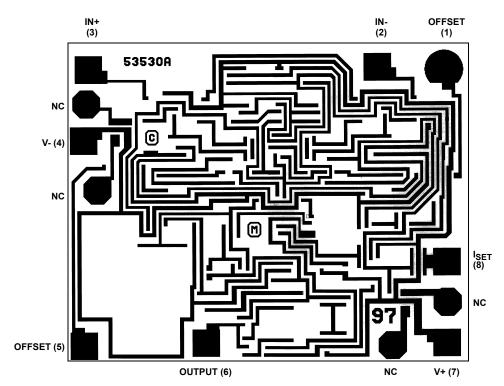
Radiation Hardened Silicon Gate, Dielectric Isolation

BACKSIDE FINISH:

Silicon

Metallization Mask Layout

Pin Numbers shown are for the Can Package HS-3530ARH, HS-3530AEH



ASSEMBLY RELATED INFORMATION

SUBSTRATE POTENTIAL:

Unbiased (DI)

ADDITIONAL INFORMATION

WORST CASE CURRENT DENSITY: <2.0 x 10⁵ A/cm²

TRANSISTOR COUNT:

49

Revision History

The revision history provided is for informational purposes only and is believed to be accurate, but not warranted. Please go to the web to make sure that you have the latest revision.

DATE	REVISION	CHANGE
February 9, 2015	FN4653.3	Added Rev History and About Intersil Verbiage. Updated datasheet by removing sentence that was not in compliance.

About Intersil

Intersil Corporation is a leading provider of innovative power management and precision analog solutions. The company's products address some of the largest markets within the industrial and infrastructure, mobile computing and high-end consumer markets.

For the most updated datasheet, application notes, related documentation and related parts, please see the respective product information page found at <u>www.intersil.com</u>.

You may report errors or suggestions for improving this datasheet by visiting www.intersil.com/ask.

Reliability reports are also available from our website at www.intersil.com/support

For additional products, see www.intersil.com/en/products.html

Intersil products are manufactured, assembled and tested utilizing ISO9001 quality systems as noted in the quality certifications found at www.intersil.com/en/support/qualandreliability.html

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